PLL (Phase-Locked Loop): A closed-loop feedback system that synchronizes output signal with input signal in frequency and phase.



→ Frequency locking of input and output ($\omega_{in} = \omega_{out}$) with constant phase difference (Θ)





VCO (Voltage Controlled Oscillator): Frequency-tunable oscillator, i.e., its output frequency is a function of a control voltage (V_C)



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PD (Phase Detector): Compares phases of input and output signal and converts the phase difference to voltage signal LPF (Low Pass Filter): Takes an average level of PD's output voltage signal







PD can be realized with a multiplier







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XOR gate can be used as PD for digital signals











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Applications of PLL: Carrier Signal Recovery



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Applications of PLL: Frequency Synthesis





Applications of PLL: Frequency Demodulation



Building Blocks: VCO



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